

Notice of References Cited

Application/Control No.

09/713,423

Applicant(s)/Patent Under
Reexamination
AGARWALA ET AL.

Examiner

Frank Duong

Art Unit

2666

Page 1 of 1

U.S. PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Name | Classification |
|---|---|--------------------------------------------------|-----------------|----------------|----------------|
| | A | US-5,014,265 | 05-1991 | Hahne et al. | 370/236 |
| | B | US-5,390,299 | 02-1995 | Rege et al. | 709/234 |
| | C | US-6,574,683 | 06-2003 | Comisky et al. | 710/23 |
| | D | US- | | | |
| | E | US- | | | |
| | F | US- | | | |
| | G | US- | | | |
| | H | US- | | | |
| | I | US- | | | |
| | J | US- | | | |
| | K | US- | | | |
| | L | US- | | | |
| | M | US- | | | |

FOREIGN PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Country | Name | Classification |
|---|---|--------------------------------------------------|-----------------|---------|------|----------------|
| | N | | | | | |
| | O | | | | | |
| | P | | | | | |
| | Q | | | | | |
| | R | | | | | |
| | S | | | | | |
| | T | | | | | |

NON-PATENT DOCUMENTS

| * | | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) |
|---|---|------------------------------------------------------------------------------------------------------------------------|
| | U | Texas Instruments, SPRU190C, TMS320C6000 Peripherals Reference Guide, Chapter 6, pages 1-41, April 1999. |
| | V | Comisky et al, A Scalable High-Performance DMA Architecture for DSP Applications, IEEE, pages 414-419, September 2000. |
| | W | |
| | X | |

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.